

Linear Extended State Observer-Based Distributed Secondary Control of DC Microgrids Under False Data Injection Attacks

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Abstract—To address the issue of unknown False Data Injection (FDI) attacks on controllers in DC microgrids, a distributed secondary controller for DC microgrids based on a linear extended state observer is presented to ensure that the system can achieve the control objectives of voltage regulation and current sharing even when subjected to FDI attacks. Firstly, the secondary control problem of multi-bus DC microgrids with FDI attacks is transformed into a first-order Multi-Agent System (MAS) fault-tolerant consistency problem, and the impact of FDI attacks on the microgrid system is analyzed. Then, a Linear Extended State Observer (LESO) is employed to detect the estimated injected FDI attack signals. Furthermore, a fault-tolerant secondary controller is designed to eliminate the adverse effects of FDI attacks on the system. The stability of the proposed method is verified using a Lyapunov function. Finally, the effectiveness and superiority of the proposed control method are experimentally demonstrated.

Index Terms—DC microgrid, distributed fault-tolerant secondary control, false data injection (FDI) attack, linear extended state observer (LESO).

I. INTRODUCTION

MICROGRIDS have emerged as a promising technology for integrating electronic interface-based renewable and non-renewable Distributed Generators (DGs), Energy Storage Systems (ESSs), and various loads, evolving into cyber-physical systems that incorporate communication, control, sensing, and computing capabilities [1], [2]. In recent years, research on DC and AC microgrids has been in full swing. Compared to AC microgrids, DC microgrids offer the following advantages: straightforward converter control, low power losses, and distributed control can be implemented more directly and efficiently without considering reactive power

and harmonics [3]. DC microgrids have attracted widespread attention from researchers in recent years and have witnessed rapid development [4].

In earlier studies, droop control was used to restore DC bus voltages and achieve current sharing, which is the basic goal of DC microgrids [5]–[7]. However, due to the presence of line impedance, droop control alone often fails to achieve accurate current sharing and may also cause voltage drift. Therefore, it is necessary to introduce secondary control to compensate for droop control. Secondary control is mainly applied to centralized or distributed control methods [8]–[10]. Centralized control methods require system information from all DGs to achieve these control objectives over a global communication network, which can lead to high communication burden and operational errors in case of communication breakdowns [11]. Distributed secondary control has been widely studied due to the fact that it does not require global information, and the use of sparse communication networks can reduce the communication burden and make the system more robust [12]. However, this distributed cooperative control approach relies on local sensing and networked control over sparse communication networks, thus making these micro-networks highly vulnerable to malicious attacks and infiltration [13]–[15].

According to the current research on cyber attacks, cyber attacks are mainly categorized into the following types: False Data Injection (FDI) Attacks, Denial of Service Attacks (DoS) and Replay Attacks [16]–[18]. Among these attacks, FDI attacks have the greatest impact on DC microgrid systems, which can lead to problems such as misjudgment and misoperation of the system, thus affecting the stability and safety of the DC microgrid. To address FDI attacks in DC microgrid systems, detecting estimating and isolating the attack signals is one of the main methods to mitigate the impact of FDI attacks on control systems. In [19], a trust-based distributed secondary control method is proposed to detect and isolate the error mesdroopes in the system using a dynamic trust estimation mechanism to minimize the impact of FDI attacks in the communication link on the microgrid system. Literature [20] designed an attack detection method based on uncoordinated elements and proposed an evaluation theory to distinguish network attacks from system failures. However, these methods to suppress FDI attack signals by detecting estimating and isolating the attack signals tend to disrupt the

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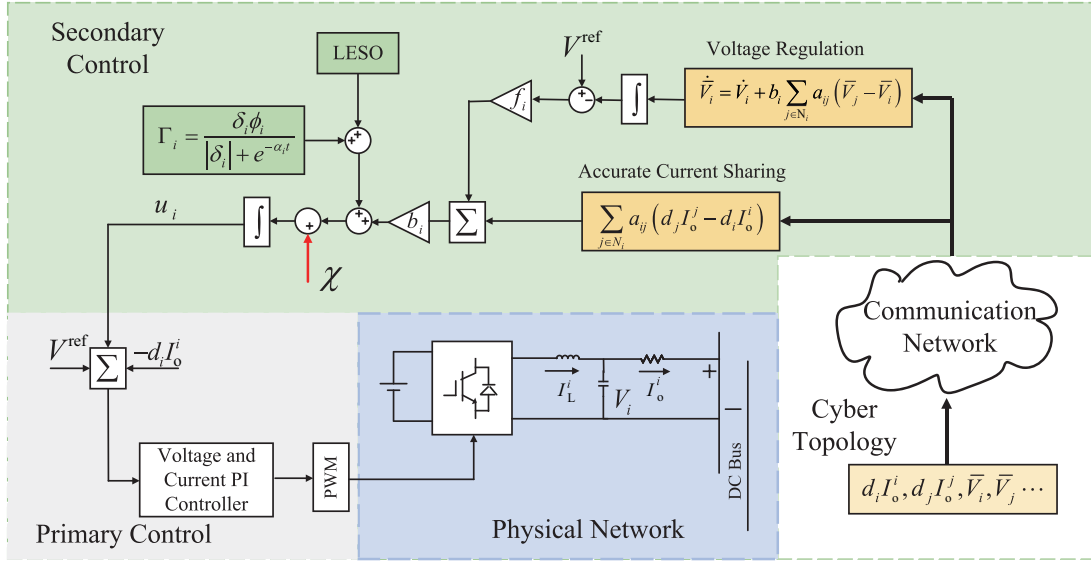


Fig. 1. Control block diagram of the given control scheme.

connectivity of the communication network and affect the consistency of the system, thus causing control bias [21]. The fault-tolerant control method, on the other hand, solves this problem. In recent years, many scholars have studied the FDI attack problem with fault-tolerant control methods. In [22], a novel distributed pulse adaptive fault-tolerant controller was constructed to address the challenges posed by spoofing attacks and actuator bias faults. In the literature [23], an adaptive fault-tolerant control scheme is proposed for a class of nonlinear strict feedback cyber-physical systems with spoofing attacks.

This paper presents a distributed fault-tolerant secondary control strategy for DC microgrids against FDI attacks. The main contributions of this paper are summarized as follows:

- A standard distributed secondary control method is reviewed, and then, by designing a new state variable, the secondary control problem of the multibus DC microgrid with attack signals is transformed into a first-order multi-intelligent body system (MAS) fault-tolerant consistency problem. The adverse effects of the attack signals on the microgrid system are also analyzed.
- A LESO is designed to detect various types of attack signals, state-space models are established, and the stability of the LESO method in DC microgrid systems is verified by pole configuration and frequency domain analysis.
- A fault-tolerant controller based on LESO is designed. The stability analysis of the global system is carried out, and the results show that the controller can enable the global system to achieve fault-tolerant consensus. Thus, it is theoretically demonstrated that the microgrid system can operate smoothly under the action of the designed controller.

The rest of the paper is structured as follows: Section II reviews distributed secondary control methods for DC microgrids and analyzes the impact of attack signals on the stability of microgrid systems. Section III presents a distributed fault-tolerant secondary control method based on LESO and

proves its stability. Section IV verifies the effectiveness and superiority of the proposed control method in DC microgrids through simulation analysis of three cases. The experimental results are given in Section V. Finally, Section VI summarizes the paper.

II. ANALYSIS OF DC MICROGRID SYSTEM BASED ON FDI ATTACK

To facilitate the illustration of the proposed control method, this section first reviews a standard distributed secondary control method and then analyzes the impact of FDI attacks on DC microgrids.

A. Graph Theory and Symbols

The communication network of a DC microgrid system can be represented by a graph $G = (W, E, A)$ with a node set W , an edge set $E \subset W \times W$, and an adjacency matrix $A = [a_{ij}]$. The graph edges represent the information flow from converter j to converter i , denoted by (w_j, w_i) and weighted by a_{ij} . If $(w_j, w_i) \in E$, then $a_{ij} = 1$; otherwise, $a_{ij} = 0$. If $(w_j, w_i) \in E$, then node j is considered to be a neighbor of node i . The set of neighbors of node i is denoted by $N_i = \{j | [(w_j, w_i) \in E]\}$. $D = \text{diag}(d_i) \in R^{N \times N}$, with $d_i = \sum_{j \in N_i} a_{ij}$, is called the in-degree matrix. $L = D - A$ denotes the Laplace matrix. G is assumed to be bidirectional, i.e., $a_{ij} = a_{ji}$. L is therefore symmetric. f_i is the pinning gain of the link from the pilot to the converter i . $f_i = 1$ if the pilot is connected to the i -converter; otherwise, $f_i = 0$. $F = \text{diag}(f_i), \forall i = 1, \dots, N$, denotes a diagonal matrix consisting of pinning gains.

B. Secondary Control Method and System Analysis Under FDI Attack

The presented secondary control is based on a multi-bus DC microgrid system. Fig. 1 shows the overall cooperative control scheme of a DG unit in an isolated DC microgrid. In the primary control of isolated DC microgrids, droop control

is usually used to realize current sharing [24]. A local reference voltage V_i^* is generated and expressed as:

$$V_i^* = V_{\text{ref}} - d_i I_o^i \quad (1)$$

where V_{ref} is the output voltage reference, d_i is the i -th droop factor, and I_o^i is the output current of the i -th converter.

In order to compensate for the voltage deviation of the droop control, it is necessary to embed the secondary control signal in (1):

$$V_i^* = V_{\text{ref}} - d_i I_o^i + u_i \quad (2)$$

where u_i is the secondary control signal of the i -th converter, the time derivative of (2) is:

$$\dot{u}_i = \dot{V}_i^* + d_i \dot{I}_o^i = e_i \quad (3)$$

which e_i is the control input. The secondary control of DC microgrids is then transformed to a consensus problem for firstorder linear MAS. To achieve voltage regulation and accurate current sharing, the secondary control based on neighboring converter information is:

$$e_i = b_i \left[f_i (V_{\text{ref}} - \bar{V}_i) + \sum_{j \in \mathcal{N}_i} a_{ij} (d_j I_o^j - d_i I_o^i) \right] \quad (4)$$

where b_i is the proportional gain. f_i is the i -th pinning gain. \bar{V}_i is the global average voltage estimate for the i -th converter that satisfies the following conditions:

$$\dot{\bar{V}}_i = \dot{V}_i + b_i \sum_{j \in \mathcal{N}_i} a_{ij} (\bar{V}_j - \bar{V}_i) \quad (5)$$

where V_i is the local output voltage.

As shown in Fig. 1, an unknown attack signal is injected to corrupt the control inputs. The dynamics of the attacked DC microgrid system can be given according to the following equation:

$$\dot{u}_i = e_i' = e_i + \chi_i \quad (6)$$

One of the control inputs that e_i' is damaged, χ_i is the attack signal, which can be modeled as a sum of finite step, ramp, or sinusoidal signals [25].

Assumption 1: For each converter, $\dot{\chi}_i$ is bounded.

Assumption 2: The attack injections considered in this paper satisfy: $\chi_i \leq \bar{\chi}_i$, where $\bar{\chi}_i$ is a positive constant.

The injected attack signals may destabilize the distributed system and thus damage the microgrid system. The effect of the attack signal on the system is analyzed theoretically below.

The local voltage controller regulates the output voltage of the converter to the reference voltage generated by the droop control with a voltage tracking error of $\varphi_i = V_i^* - V_i$, then $V_i^* = V_i + \varphi_i$, the time derivative of V_i^* can be derived as:

$$\dot{V}_i^* = \dot{V}_i + \dot{\varphi}_i \quad (7)$$

Substituting (6) and (7) into (3) yields:

$$\dot{V}_i + d_i \dot{I}_o^i + \dot{\varphi}_i = e_i + \chi_i \quad (8)$$

From (3)-(5) and (8), we can get:

$$\begin{aligned} \dot{\bar{V}}_i + d_i \dot{I}_o^i &= b_i \sum_{j \in \mathcal{N}_i} a_{ij} \left[(\bar{V}_j + d_j \dot{I}_o^j) - (\bar{V}_i + d_i \dot{I}_o^i) \right] + \\ & b_i f_i \left[d_i \dot{I}_o^i - (\bar{V}_i + d_i \dot{I}_o^i) \right] - \dot{\varphi}_i + \chi_i \end{aligned} \quad (9)$$

$d_i \dot{I}_o^i$ converges to a constant I_{ss} in the steady state. Let $X_i = \bar{V}_i + d_i \dot{I}_o^i$, $X_{\text{ref}} = I_{\text{ss}}$, then (10) can be rewritten as:

$$\dot{X}_i = b_i \left[\sum_{j \in \mathcal{N}_i} a_{ij} (X_j - X_i) + f_i (X_{\text{ref}} - X_i) \right] - \dot{\varphi}_i + \chi_i \quad (10)$$

Remark 1: In this paper, we transform the distributed secondary control problem of a DC microgrid into a first-order MAS consistency problem. This is because in the secondary control, each DG unit needs to exchange its respective voltage and current information, of which \bar{V}_i is easy to observe and $d_i \dot{I}_o^i$ is hard to observe. In this paper, we design a new state variable X_i that converts the exchanged information (\bar{V}_i and $d_i \dot{I}_o^i$) into \bar{V}_i and X_i , which allows us to derive the multi-bus DC microgrid model with FDI attack into a first-order MAS model. The MAS model is then used to analyze and design the controller.

Defining the state variable error as $\varepsilon_i = X_i - X_{\text{ref}}$, the time derivative of ε_i is:

$$\dot{\varepsilon}_i = -b_i \left[\sum_{j \in \mathcal{N}_i} a_{ij} (\varepsilon_j - \varepsilon_i) - f_i \varepsilon_i \right] - \dot{\varphi}_i + \chi_i \quad (11)$$

Let $\varepsilon = [\varepsilon_1, \varepsilon_2, \dots, \varepsilon_n]^T$, then the global form of (11) can be expressed as:

$$\dot{\varepsilon} = -\mathbf{B}(\mathbf{L} + \mathbf{F})\varepsilon - \boldsymbol{\psi} + \mathbf{X} \quad (12)$$

where $\mathbf{B} = \text{diag}(b_i)$, $\boldsymbol{\psi} = [\dot{\varphi}_1, \dot{\varphi}_2, \dots, \dot{\varphi}_n]^T$, $\mathbf{X} = [\chi_1, \chi_2, \dots, \chi_n]^T$, $\mathbf{F} = \text{diag}(f_i)$. The dynamic expression can be derived:

$$\begin{aligned} \varepsilon(t) &= \exp[-\mathbf{B}(\mathbf{L} + \mathbf{F})t] \cdot \varepsilon(t_0) + \\ & \int_{t_0}^t \exp[-\mathbf{B}(\mathbf{L} + \mathbf{F})(t - \tau)] \cdot [\mathbf{X}(\tau) - \boldsymbol{\psi}(\tau)] d\tau \end{aligned} \quad (13)$$

Without loss of generality, the attack signal is assumed to be positive, $\chi(\tau) > \chi_0 > 0$. It is assumed that the system can converge to the reference voltage, i.e., $\lim_{t \rightarrow \infty} \varphi(t) = 0$, by means of a local voltage controller. Moreover, since $\mathbf{B}(\mathbf{L} + \mathbf{F})$ is a positive definite invertible matrix, $\exp[-\mathbf{B}(\mathbf{L} + \mathbf{F})t] \cdot \varepsilon(t_0)$ can converge to zero. Therefore, it is possible to obtain:

$$\begin{aligned} \lim_{t \rightarrow \infty} \varepsilon(t) &= \lim_{t \rightarrow \infty} \int_{t_0}^t \exp[-\mathbf{B}(\mathbf{L} + \mathbf{F})(t - \tau)] \cdot \mathbf{X}(\tau) d\tau > \\ & \exp[-\mathbf{B}(\mathbf{L} + \mathbf{F})t] \cdot \exp[\mathbf{B}(\mathbf{L} + \mathbf{F})t] - \exp[\mathbf{B}(\mathbf{L} + \mathbf{F})t_0] \cdot \\ & [\mathbf{B}(\mathbf{L} + \mathbf{F})]^{-1} \chi_0 = [\mathbf{B}(\mathbf{L} + \mathbf{F})]^{-1} \chi_0 \geq 0 \end{aligned} \quad (14)$$

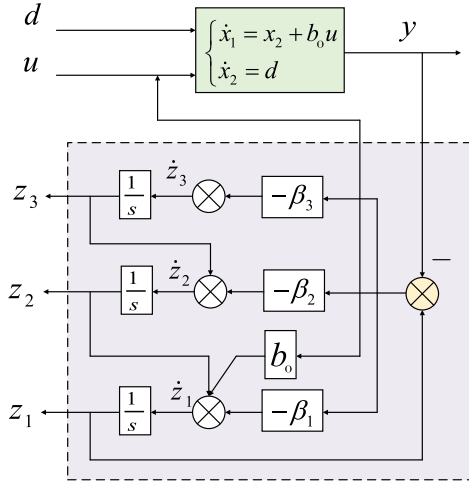


Fig. 2. Linear extended state observer.

From (14), the state error of the system fails to converge to zero under the influence of the FDI attack signal, i.e., it fails to achieve the desired goal of voltage regulation and current sharing.

III. A DISTRIBUTED FAULT-TOLERANT CONTROL METHOD BASED ON LESO IS GIVEN

This section details the given distributed fault-tolerant secondary control method based on LESO. Based on the system analysis, this paper transforms the control problem of DC microgrids into a first-order MAS fault-tolerant consistency problem. Firstly, LESO is designed to detect the estimated FDI attack signals. Then, a distributed fault-tolerant controller based on LESO is given to compensate and suppress the FDI attack. Finally, the stability of the designed LESO and fault-tolerant controller is verified.

A. Linear Extended State Observer

In order to detect and estimate FDI attack signals, the model of LESO is given in this part, as shown in Fig. 2.

From (6), considering u_i , χ as system state variables, the corresponding state space equation can be constructed as [26]:

$$\begin{cases} \dot{x}_1 = x_2 + b_0 u \\ \dot{x}_2 = x_3 \\ \dot{x}_3 = h \\ y = x_1 \end{cases} \quad (15)$$

where $x_1 = u_i$, $x_2 = \chi$, $b_0 = 1$, and e is the control input u . Extending the derivative term of the attack as a new state variable, that is $\dot{\chi} = x_3$, to increase bandwidth and reduce the number of communications. Then (15) is written in matrix form as:

$$\begin{cases} \dot{x} = Ax + Bu + Eh \\ y = Cx \end{cases} \quad (16)$$

$$\text{where } A = \begin{bmatrix} 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 0 \end{bmatrix}, B = \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix}, E = \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix}, C = [1 \ 0 \ 0].$$

By the introduction of the extended state, the original system is changed from the original second-order system to the third-order system, then the design of the extended state observer can be expressed as:

$$\begin{cases} \dot{z} = Az + Bu - L(z_1 - y) \\ y = Cz \end{cases} \quad (17)$$

where the observation matrix $L = [\beta_1 \ \beta_2 \ \beta_3]^T$, then the error matrix relationship can be expressed as:

$$\dot{e} = A_e e - Eh \quad (18)$$

$$\text{where } A_e = A - LC = \begin{bmatrix} -\beta_1 & 1 & 0 \\ -\beta_2 & 0 & 1 \\ -\beta_3 & 0 & 0 \end{bmatrix}$$

The system is stable if the chosen roots A_e are in the left half plane. To make the design process easy to implement, assuming that the observer poles are all located at $-\omega_o$, the characteristic polynomial can be designed as:

$$u(s) = |sI - A_e| = s^3 + \beta_1 s^2 + \beta_2 s + \beta_3 = (s + \omega_o)^3 \quad (19)$$

where ω_o is the bandwidth of the LESO. $\beta_1 = 3\omega_o$, $\beta_2 = 3\omega_o^2$, $\beta_3 = \omega_o^3$.

B. Designed A Distributed Fault-Tolerant Controller

This section details the designed distributed fault-tolerant secondary controller and verifies the stability of the control system. Based on the derived MAS and the observed FDI attack signals, the distributed fault-tolerant secondary controller is designed to eliminate the adverse effects of the attack signals on the system.

For the sake of convenience, we denote:

$$\delta_i = b_i \left(\sum_{j \in N_i} a_{ij} (X_j - X_i) + f_i (X_{ref} - X_i) \right) \quad (20)$$

To ensure voltage regulation and proportional load sharing under FDI attacks, we design the following distributed fault-tolerant secondary controller [27]:

$$e_i = b_i \left[f_i (V_{ref} - \bar{V}_i) + \sum_{j \in N_i} a_{ij} (d_j I_o^j - d_i I_o^i) \right] + \Gamma_i \quad (21)$$

$$\Gamma_i = \frac{\delta_i \phi_i}{|\delta_i| + e^{-\alpha t}} \quad (22)$$

$$\dot{\phi}_i = u_i |\delta_i| \quad (23)$$

where Γ_i is the fault-tolerance compensation term, ϕ_i is the fault-tolerance parameter, α_i and μ_i are positive constants. For convenience, set $\mu_i \geq 1$. The consistent continuous function $e^{-\alpha t}$ provides a smooth control method for practical implementation. Next, the stability of the designed controller is analyzed.

The time derivative of (20) can be obtained using (5), (6), and (21).

$$\begin{aligned}
\dot{\delta}_i &= b_i \left[\sum_{j \in N_i} a_{ij} (\dot{X}_j - \dot{X}_i) + f_i (\dot{X}_{\text{ref}} - \dot{X}_i) \right] \\
&= -b_i (d_i + f_i) \dot{X}_i + b_i \sum_{j \in N_i} a_{ij} \dot{X}_j \\
&= -b_i (d_i + f_i) (\delta_i + \chi_i + \Gamma_i) + b_i \sum_{j \in N_i} a_{ij} (\delta_j + \chi_j + \Gamma_j)
\end{aligned} \tag{24}$$

Denote $Y_i = \chi_i - \frac{1}{(d_i + f_i)} \sum_{j \in N_i} a_{ij} (\delta_j + \chi_j + \Gamma_j)$, then (24) can be rewritten as:

$$\dot{\delta}_i = -b_i (d_i + f_i) (\delta_i + \chi_i + \Gamma_i) \tag{25}$$

Consider the following Lyapunov candidate function:

$$V_i = \frac{1}{2} \left(|\delta_i| + \frac{d|Y_i|}{dt} \right)^2 \tag{26}$$

Its time derivative is given as:

$$\dot{V}_i = \left(|\delta_i| - \frac{d|Y_i|}{dt} \right) \left(\frac{d|\delta_i|}{dt} - \frac{d^2|Y_i|}{dt^2} \right) \tag{27}$$

where $\frac{d|Y_i|}{dt} = \frac{Y_i \dot{Y}_i}{|Y_i|} \leq |\dot{Y}_i|$, note that

$$\begin{aligned}
\frac{d|\delta_i|}{dt} &= \frac{\delta_i \dot{\delta}_i}{|\delta_i|} = \frac{-b_i (d_i + f_i) \delta_i (\delta_i + \chi_i + \Gamma_i)}{|\delta_i|} \\
&= -b_i (d_i + f_i) \left(|\delta_i| + \frac{\delta_i Y_i}{|\delta_i|} + \frac{\delta_i \Gamma_i}{|\delta_i|} \right)
\end{aligned} \tag{28}$$

Substituting (28) into (27) yields:

$$\begin{aligned}
\dot{V}_i &= \left(|\delta_i| - \frac{d|Y_i|}{dt} \right) \times \\
&\left[-b_i (d_i + f_i) |\delta_i| - \frac{d^2|Y_i|}{dt^2} - b_i (d_i + f_i) \left(\frac{\delta_i Y_i}{|\delta_i|} + \frac{\delta_i \Gamma_i}{|\delta_i|} \right) \right]
\end{aligned} \tag{29}$$

Use (22) to get:

$$\begin{aligned}
&-b_i (d_i + f_i) \left(\frac{\delta_i Y_i}{|\delta_i|} + \frac{\delta_i \Gamma_i}{|\delta_i|} \right) = \\
&-b_i (d_i + f_i) \frac{\delta_i Y_i}{|\delta_i|} - b_i (d_i + f_i) \frac{|\delta_i| \phi_i}{|\delta_i| + e^{-at}} \leq \\
&b_i (d_i + f_i) |Y_i| - b_i (d_i + f_i) \frac{|\delta_i| \phi_i}{|\delta_i| + e^{-at}} \leq \\
&b_i (d_i + f_i) \frac{|Y_i| |\delta_i| + e^{-at} |Y_i| - |\delta_i| \phi_i}{|\delta_i| + e^{-at}}
\end{aligned} \tag{30}$$

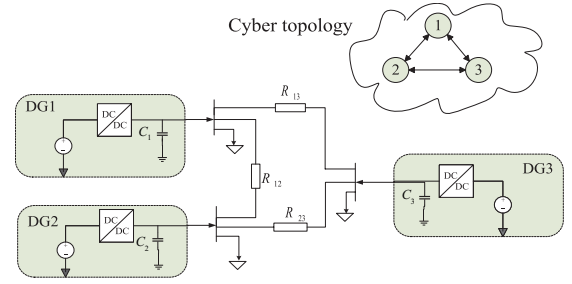


Fig. 3. DC microgrid system consisting of 3 DGs.

TABLE I
PARAMETER SETTING

Parameters	Value	Parameters	Value
V_{DC}	100 V	f_i	25
V_{ref}	48 V	b_i	20
f_s	100 KHz	d_i	1
L	1.8 mH	k_{vp}/k_{vi}	1/100
C	2.2 mF	k_{ip}/k_{ii}	0.5/10
T_{ij}	1 Ω	α_i	0.1
R	5 Ω	μ_i	5
W_o	200 rad/s		

Choosing $|\delta_i| > \frac{d|Y_i|}{dt}$ gets $\dot{\phi}_i > \frac{d|Y_i|}{dt} \cdot e^{-at} |Y_i| \rightarrow 0$. Thus, $\exists \tau_1 > 0$, that makes for all $t > \tau_1$, we have:

$$|Y_i| |\delta_i| + e^{-at} |Y_i| - |\delta_i| \phi_i \leq 0 \tag{31}$$

Then, (30) and (31) are utilized to get:

$$-b_i (d_i + f_i) \left(\frac{\delta_i Y_i}{|\delta_i|} + \frac{\delta_i \Gamma_i}{|\delta_i|} \right) < 0, \quad t > \tau_1 \tag{32}$$

Combining (29) and (32), the selection

$$|\delta_i| \geq -\frac{1}{b_i (d_i + f_i)} \frac{d^2|Y_i|}{dt^2} \text{ yields:}$$

$$\begin{aligned}
\dot{V}_i &\leq \left(|\delta_i| - \frac{d|Y_i|}{dt} \right) \left[-b_i (d_i + f_i) |\delta_i| - \frac{d^2|Y_i|}{dt^2} \right] \\
&\leq 0, \quad \forall t > \tau_1
\end{aligned} \tag{33}$$

$\dot{V}_i = 0$ if and only if $|\delta_i| = 0$. Therefore, the designed fault-tolerant controller can ensure the fault-tolerant consistency of the MAS, i.e., the DC microgrid system under FDI attack can maintain stable operation.

IV. SIMULATION RESULTS

In order to verify the effectiveness of the given fault-tolerant control method, an islanded multi-bus DC microgrid system

TABLE II
ATTACK SIGNALS IN THE THREE CASES

Case	Attack note	Attack signal	t_{attack}
Case 1	DG1	10	$t = 0.2-1$ s
	DG3	$10 \sin(100 \pi t)$	$t = 0.4-1$ s
Case 2	DG1	$20 \sin(200 \pi t) + 20$	$t = 0.2-1$ s
	DG2	$-20t$	$t = 0.4-1$ s
Case 3	DG2	6	$t = 0.2-1$ s
	DG3	$30 \sin(300 \pi t + \pi) + 30$	$t = 0.4-1$ s

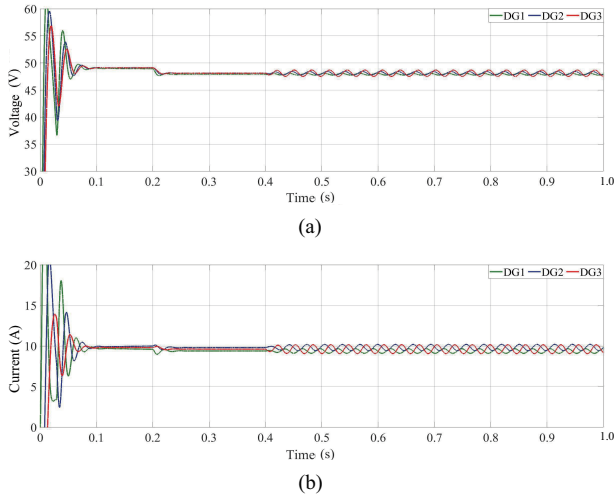


Fig. 4. The results of not configuring the fault-tolerant controller under the attack signal are: (a) output voltage, (b) output current.

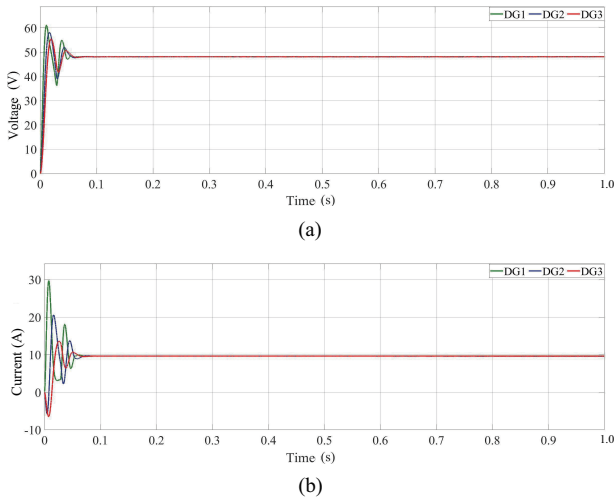


Fig. 5. The results of using fault tolerant controller under attack signal are: (a) output voltage, (b) output current.

with three DG units is constructed using MATLAB/Simulink as shown in Fig. 3. The parameters of the DGs and controllers are shown in Table I. The attack signals of each DG in different cases are shown in Table II [25], where t_{attack} is the time interval of FDI attack.

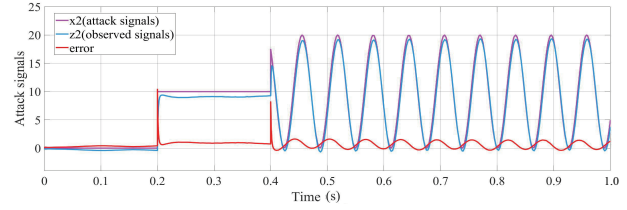


Fig. 6. Observed values of attack signals of case 1.

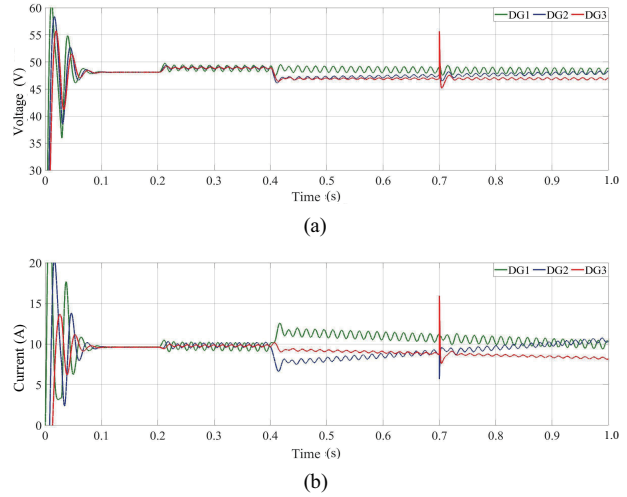


Fig. 7. The results of not configuring the fault-tolerant controller at $t = 0.7$ s communication interruption under attack signal are: (a) output voltage, (b) output current.

A. Case 1: Performance Comparison Under FDI Attack

In this section, the effectiveness of the given controller under network attack is simulated. The simulation process takes 1 s. A step attack signal $\chi_1 = 10$ is injected into DG1 at $t = 0.2$ s and a sinusoidal attack signal $\chi_2 = 10 \sin(100 \pi t)$ is injected into DG3 at $t = 0.4$ s. In this experiment, the given control strategies are compared with and without fault-tolerant controllers. The experimental results are shown in Figs. 4–6.

As shown in Fig. 4, injecting the attack signal into the control input channel at 0.2 s and 0.4 s, the DC microgrid system undergoes significant fluctuations. However, after adopting the fault-tolerant control method, as shown in Fig. 5, the system quickly converges to a steady-state value. Moreover, the output voltage of each converter is maintained at 48 V in the presence of the attack signal and the output current is accurately shared. The observed signals of the attack as well as the observation errors are shown in Fig. 6. It can be seen that the given LESO can track the attack signal quickly.

Therefore, the given fault tolerant control method is effective for general attack signals and ensures voltage regulation and current sharing.

B. Case 2: Communication Interruption

This section simulates and analyzes the performance of the designed fault-tolerant controller under communication interruption. In order to better analyze the performance of

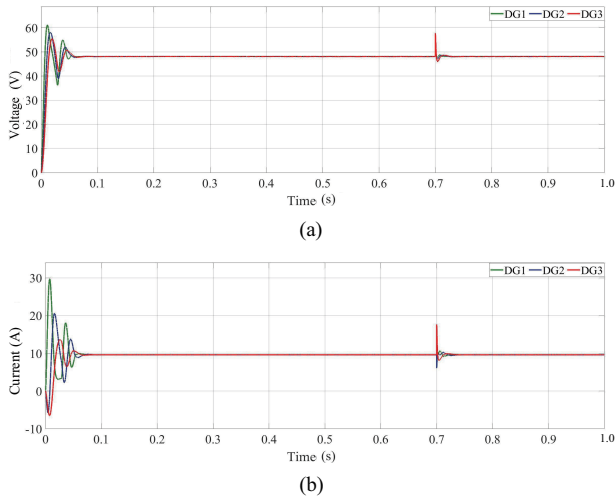


Fig. 8. The results of using fault-tolerant controller at $t = 0.7$ s communication interruption under attack signal are: (a) output voltage, (b) output current.

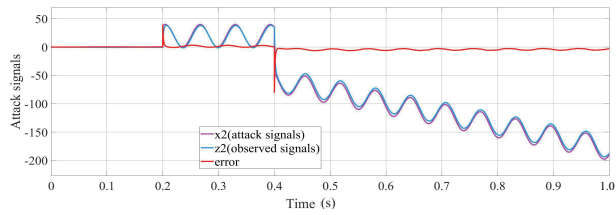


Fig. 9. Observed values of attack signals of case 2.

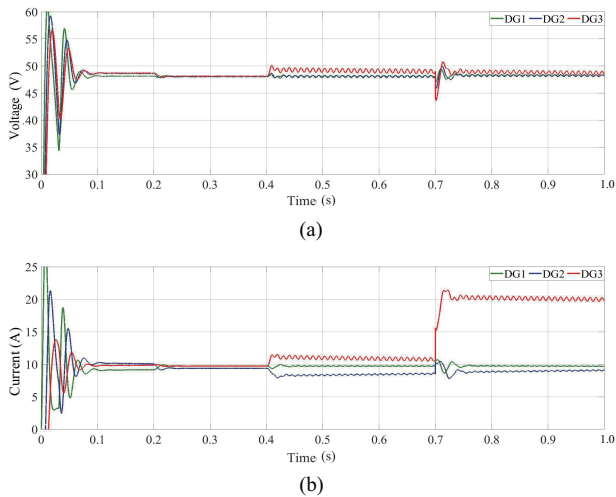


Fig. 10. The results of not configuring the fault-tolerant controller at $t = 0.7$ s load change under attack signal are: (a) output voltage, (b) output current.

the fault-tolerant controller in the case of communication interruption, a sinusoidal attack signal $\chi_1 = 20\sin(200\pi t) + 20$ is injected into DG1 at $t = 0.2$ s and a ramp attack signal $\chi_2 = -20t$ is injected into DG2 at $t = 0.4$ s. At $t = 0.7$ s, the communication between DG2 and DG3 is interrupted. The experimental results are shown in Figs. 7–9.

As can be seen in Figs. 7 and 8, the system stabilizes under the fault-tolerant controller at 0.2 s and 0.4 s injection attacks.

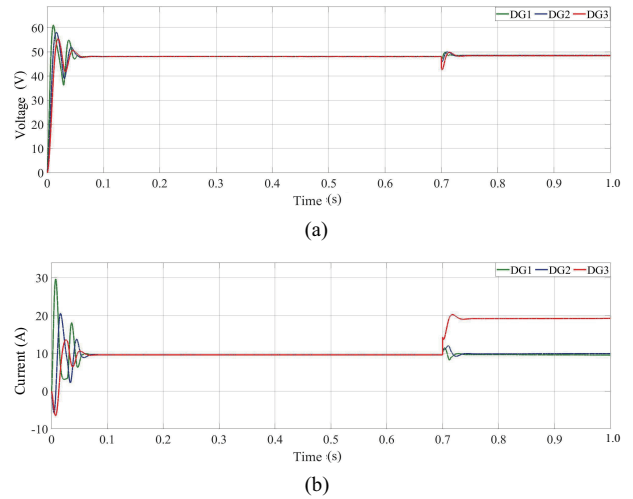


Fig. 11. The results of using fault-tolerant controller at $t = 0.7$ s load change under attack signal are: (a) output voltage, (b) output current.

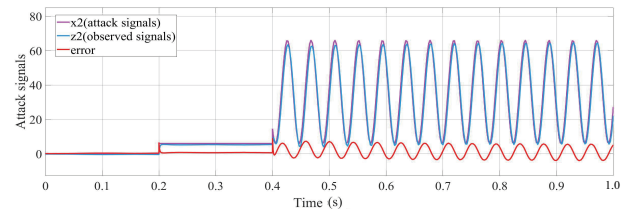


Fig. 12. Observed values of attack signals of case 3.

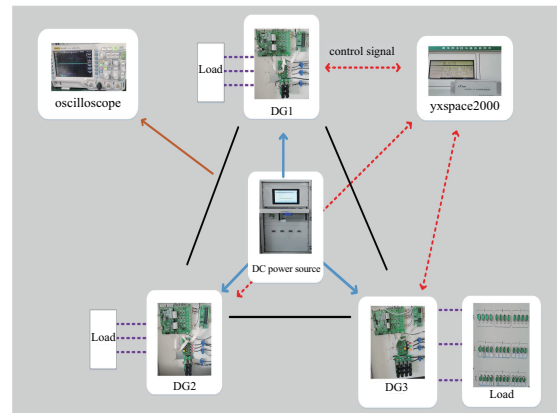


Fig. 13. Experimental setup.

Under the effect of 0.7 s communication interruption, both Fig. 7 and Fig. 8 have short fluctuations, indicating that the DC microgrid system without fault-tolerant controllers is inherently robust. In addition, it can be seen from Fig. 9 that the attack signal is successfully tracked.

C. Case 3: Load Variation

In this section, the system response of the given fault-tolerant secondary controller under load variation and network attack is simulated and analyzed. A step attack signal $\chi_1 = 6$ is injected into DG2 at $t = 0.2$ s and a sinusoidal attack signal

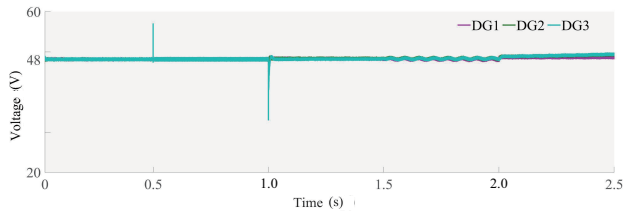


Fig. 14. Output voltage of experimental results.

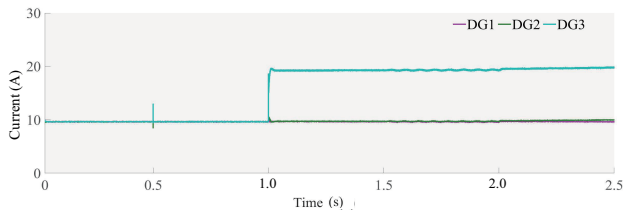


Fig. 15. Output current of experimental results.

$\chi_2 = 30\sin(300\pi t + \pi) + 30$ is injected into DG3 at $t = 0.4$ s. The load resistance of DG3 is reduced to 2.5Ω at $t = 0.7$ s. The experimental results are shown in Figs. 10–12.

As can be observed from Fig. 10(a) and Fig. 11(a), it can be concluded that the system output voltage stabilizes under the fault-tolerant controller at 0.2 s and 0.4 s injection attacks; from Fig. 10(b) and Fig. 11(b), there is a sudden change in the output current of DG3 due to the load change. Under the effect of 0.7 s communication interruption, there is a short fluctuation in both Fig. 7 and Fig. 8, which also illustrates that the DC microgrid system without fault-tolerant controller is inherently robust. In addition, it can be seen from Fig. 12 that the tracking signal of the attack is good.

V. EXPERIMENTAL RESULTS

The performance of the proposed fault-tolerant control scheme has been experimentally verified on a microgrid semiphysical platform consisting of 3 DGs and the YXSPACE2000 control platform, as shown in Fig. 13. The nominal voltage for the DC microgrid is 48 V. Besides, the line impedance of DGs are $r_{ij} = 5 \Omega$. The control loop uses the scheme shown in Fig. 1 to regulate the voltage and current of each DG.

For convenience, all the results described above are represented in a single experiment via a microgrid semiphysical platform. In Figs. 14 and 15, the communication between DG2 and DG3 is interrupted at $t = 0.5$ s, at this time, the voltage and current fluctuate slightly. The load resistance of DG3 is reduced to 2.5Ω at $t = 1$ s, there is a fluctuation in voltage and the current in DG3 doubles. At $t = 1.5$ s, a sinusoidal attack signal is injected into DG1, the system continues to fluctuate. At $t = 2$ s, the system incorporates the fault tolerant controller given in this paper, the system is restored to stability. It can be seen that system normal regulation signal do not continuously affect the system stability. When the attack signal is added, the system is dysregulated and external intervention is required to stabilize the system.

Therefore, the fault-tolerance of the given scheme is verified.

VI. CONCLUSION

This paper presents a distributed LESO-based fault-tolerant secondary control method for DC microgrids, which enables voltage regulation and accurate current sharing in the presence of FDI attacks. LESO is utilized to estimate the attack signals in the DG, and a distributed fault-tolerant secondary controller is employed to compensate for the adverse effects of the attack signals on the system, with the capability to detect and suppress FDI attacks. A rigorous proof based on the Lyapunov function is provided. Moreover, the controller is robust against communication interruptions and load variations, further demonstrating the effectiveness and superiority of the method in DC microgrids. The controller designed in this paper primarily focuses on the attack signals in the actuators, and the attack signals in the sensors will be analyzed and investigated in the following step.

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